



H0921

PATENT

- 1 -

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Mahanpour
Serial No.: 10/701,877
Filed: November 5, 2003
Group Art Unit: 2822
Before the Examiner: Kevin M. Picardat
Title: METHOD AND SYSTEM FOR PROVIDING BACKSIDE VOLTAGE
CONTRAST FOR SILICON ON INSULATOR DEVICES

Date of mailing of PTOL-85 entitled "Notice of Allowance and Issue Fee Due": 09/02/2005

TRANSMITTAL OF FORMAL DRAWINGS

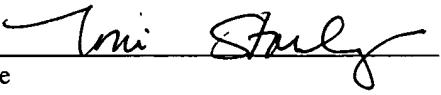
Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Applicants note on the PTOL-37 Form - Notice of Allowability, the Examiner accepted the drawings filed on November 5, 2003. On November 5, 2003 the drawings submitted were informal. Applicants subsequently submitted formal drawings on March 22, 2004. Applicants submit herewith the formal drawings for this application. Number of sheets of drawings submitted: EIGHT (8).

CERTIFICATION UNDER 37 C.F.R. § 1.8

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to the Mail Stop Issue Fee, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on 11-11, 2005.


Signature

Toni Stanley
(Printed name of person certifying)

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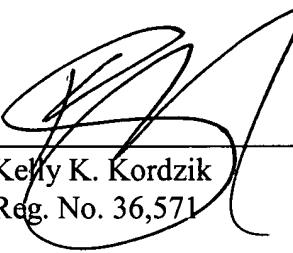
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The three-month period of response set in the Notice of Allowability (PTOL-37) expires on December 2, 2005. This submission is before this expiry date.

Respectfully submitted,

WINSTEAD SECHREST & MINICK P.C.

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By: 

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